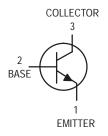
# **General Purpose Transistors NPN Silicon**

2N3903 2N3904\*

\*Motorola Preferred Device





### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	VCEO	40	Vdc
Collector-Base Voltage	Vсво	60	Vdc
Emitter-Base Voltage	VEBO	6.0	Vdc
Collector Current — Continuous	IC	200	mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	1.5 12	Watts mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

### THERMAL CHARACTERISTICS(1)

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	83.3	°C/W

### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage (2) (I <sub>C</sub> = 1.0 mAdc, I <sub>B</sub> = 0)	V(BR)CE	0 40	_	Vdc
Collector-Base Breakdown Voltage (I <sub>C</sub> = 10 μAdc, I <sub>E</sub> = 0)	V(BR)CB	60	_	Vdc
Emitter-Base Breakdown Voltage ( $I_E = 10 \mu Adc, I_C = 0$ )	V(BR)EB	6.0	_	Vdc
Base Cutoff Current (V <sub>CE</sub> = 30 Vdc, V <sub>EB</sub> = 3.0 Vdc)	I <sub>BL</sub>	_	50	nAdc
Collector Cutoff Current (V <sub>CE</sub> = 30 Vdc, V <sub>EB</sub> = 3.0 Vdc)	ICEX	_	50	nAdc

- 1. Indicates Data in addition to JEDEC Requirements.
- 2. Pulse Test: Pulse Width  $\leq$  300  $\mu$ s; Duty Cycle  $\leq$  2.0%.

Preferred devices are Motorola recommended choices for future use and best overall value.

REV 2



### 2N3903 2N3904

### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25$ °C unless otherwise noted) (Continued)

	Characteristic		Symbol	Min	Max	Unit
ON CHARACTERIS	STICS					
DC Current Gain <sup>(1)</sup> (I <sub>C</sub> = 0.1 mAdc, V <sub>C</sub>	<sub>DE</sub> = 1.0 Vdc)	2N3903 2N3904	hFE	20 40	_	_
$(I_C = 1.0 \text{ mAdc}, V_C)$	CE = 1.0 Vdc)	2N3903 2N3904		35 70	_	
$(I_C = 10 \text{ mAdc}, V_C)$	e <sub>E</sub> = 1.0 Vdc)	2N3903 2N3904		50 100	150 300	
$(I_C = 50 \text{ mAdc}, V_C)$	<sub>E</sub> = 1.0 Vdc)	2N3903 2N3904		30 60	_	
$(I_C = 100 \text{ mAdc}, V_c)$	CE = 1.0 Vdc)	2N3903 2N3904		15 30	_	
Collector-Emitter Sa (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = (I <sub>C</sub> = 50 mAdc, I <sub>B</sub> = 10 mAdc,	= 1.0 mAdc)		VCE(sat)	=	0.2 0.3	Vdc
Base-Emitter Satura (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> and I <sub>C</sub> = 50 mAdc,	= 1.0 mAdc)		V <sub>BE</sub> (sat)	0.65 —	0.85 0.95	Vdc
SMALL-SIGNAL C	CHARACTERISTICS		•		•	-
Current-Gain — Bar (I <sub>C</sub> = 10 mAdc, V <sub>C</sub>	ndwidth Product c <sub>E</sub> = 20 Vdc, f = 100 MHz)	2N3903 2N3904	fΤ	250 300	_	MHz
Output Capacitance (V <sub>CB</sub> = 5.0 Vdc, I <sub>E</sub>	= 0, f = 1.0 MHz)		C <sub>obo</sub>	_	4.0	pF
Input Capacitance (VEB = 0.5 Vdc, IC	c = 0, f = 1.0 MHz)		C <sub>ibo</sub>	_	8.0	pF
Input Impedance (I <sub>C</sub> = 1.0 mAdc, V <sub>C</sub>	CE = 10 Vdc, f = 1.0 kHz)	2N3903 2N3904	h <sub>ie</sub>	1.0 1.0	8.0 10	kΩ
Voltage Feedback Ra (I <sub>C</sub> = 1.0 mAdc, V <sub>C</sub>	atio CE = 10 Vdc, f = 1.0 kHz)	2N3903 2N3904	h <sub>re</sub>	0.1 0.5	5.0 8.0	X 10 <sup>-4</sup>
Small–Signal Current (I <sub>C</sub> = 1.0 mAdc, V <sub>C</sub>	t Gain CE = 10 Vdc, f = 1.0 kHz)	2N3903 2N3904	h <sub>fe</sub>	50 100	200 400	_
Output Admittance (I <sub>C</sub> = 1.0 mAdc, V <sub>C</sub>	DE = 10 Vdc, f = 1.0 kHz)		h <sub>oe</sub>	1.0	40	μmhos
Noise Figure (I <sub>C</sub> = 100 μAdc, V <sub>C</sub>	$\Omega$ E = 5.0 Vdc, R <sub>S</sub> = 1.0 k Ω, f = 1.0 kHz)	2N3903 2N3904	NF	_ 	6.0 5.0	dB
SWITCHING CHAR	RACTERISTICS					
Delay Time	(V <sub>CC</sub> = 3.0 Vdc, V <sub>BE</sub> = 0.5 Vdc,		<sup>t</sup> d	_	35	ns
Rise Time	I <sub>C</sub> = 10 mAdc, I <sub>B1</sub> = 1.0 mAdc)		t <sub>r</sub>	_	35	ns
Storage Time	(V <sub>CC</sub> = 3.0 Vdc, I <sub>C</sub> = 10 mAdc, I <sub>B1</sub> = I <sub>B2</sub> = 1.0 mAdc)	2N3903 2N3904	t <sub>S</sub>	_	175 200	ns
Fall Time			t <sub>f</sub>	_	50	ns

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s; Duty Cycle  $\leq$  2.0%.

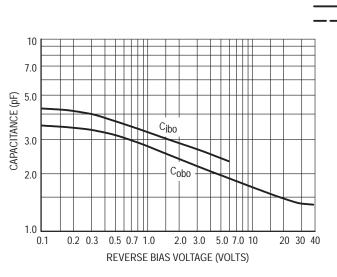
\* Total shunt capacitance of test jig and connectors

Figure 1. Delay and Rise Time Equivalent Test Circuit

Figure 2. Storage and Fall Time Equivalent Test Circuit

### **TYPICAL TRANSIENT CHARACTERISTICS**

T<sub>J</sub> = 25°C



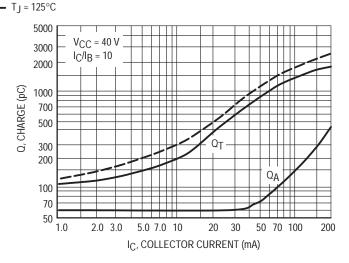


Figure 3. Capacitance

Figure 4. Charge Data

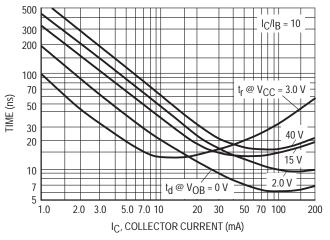


Figure 5. Turn-On Time

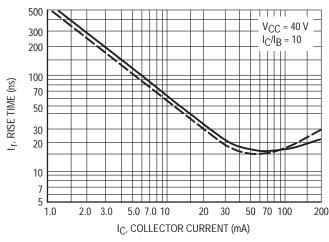


Figure 6. Rise Time

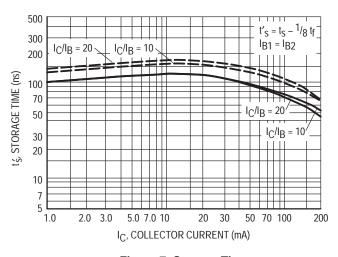


Figure 7. Storage Time

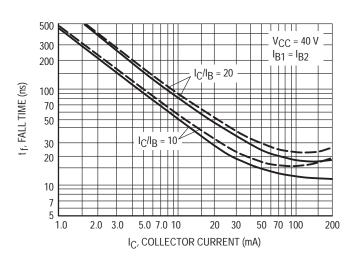


Figure 8. Fall Time

## TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

 $(VCE = 5.0 \text{ Vdc}, T_A = 25^{\circ}\text{C}, Bandwidth = 1.0 \text{ Hz})$ 

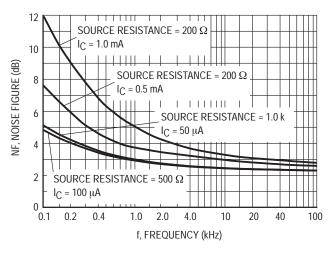


Figure 9.

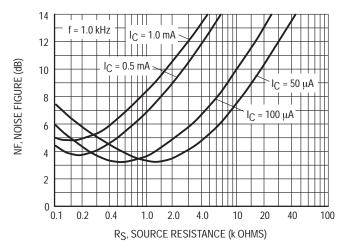
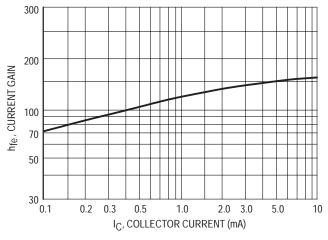


Figure 10.

### h PARAMETERS

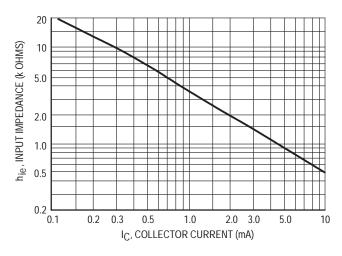
 $(VCE = 10 Vdc, f = 1.0 kHz, TA = 25^{\circ}C)$ 



100 h<sub>0e</sub>, OUTPUT ADMITTANCE (μmhos) 50 20 10 5 2 0.1 0.2 0.3 0.5 1.0 2.0 3.0 5.0 10 I<sub>C</sub>, COLLECTOR CURRENT (mA)

Figure 11. Current Gain

Figure 12. Output Admittance



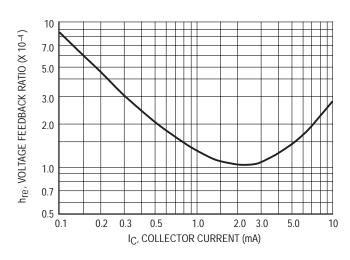


Figure 13. Input Impedance

Figure 14. Voltage Feedback Ratio

### TYPICAL STATIC CHARACTERISTICS

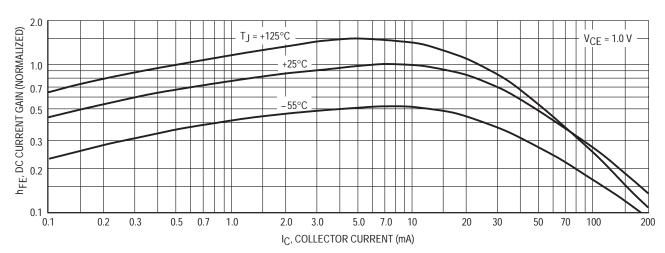


Figure 15. DC Current Gain

### 2N3903 2N3904

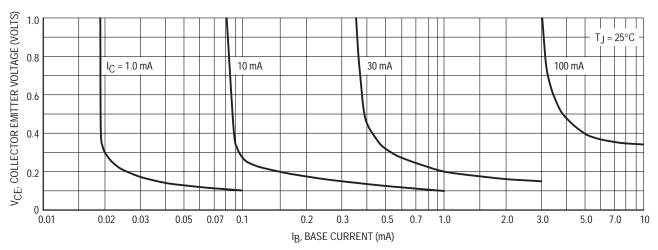


Figure 16. Collector Saturation Region

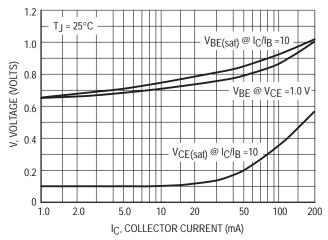


Figure 17. "ON" Voltages

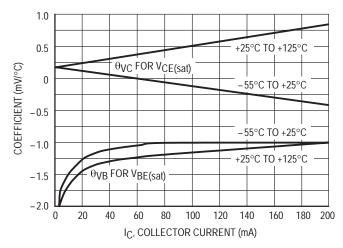
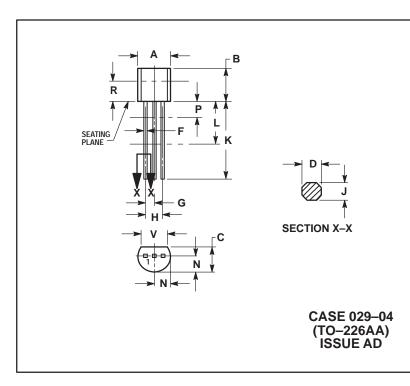


Figure 18. Temperature Coefficients

### **PACKAGE DIMENSIONS**



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
  4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSION D AND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

STYLE 1: PIN 1. EMITTER

BASE
 COLLECTOR

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